

TESS Camera Radiation Event Data

Latest data: 18-Jun-19

The FPGA is XQ7A50T-FG484 (Artix-7) with on-chip Xilinx single-bit correction/multi bit detection
 Orbit is Interplanetary; memories have EDAC
 Double bar indicates an FPGA power cycle/reload; every LAHO after 2/28/19 has a reload

Totals	
Days in orbit with scrubber enabled	308
Corrected Configuration Events	150
Uncorrected Configuration Events	11
Sequencer RAM Events	3
Average # of days per FPGA per SEU	7.5

Notes:

After the first uncorrectable error occurs, the error counter may or may not increment wildly. The correctable count applies only for the preceding interval, and so is a minimum.

Sector Num	Orbit Num	Orbit Start	Orbit End	Camera #	Config Cor	Config Uncor	SEQ	Start Date	Start Hours	End Date	End Hours	Days Off	Notes
1	9	26-Jul-18	8-Aug-18	1									(data not available)
				2									
				3									
				4									
	10	9-Aug-18	22-Aug-18	1	5	0	0						
				2	1	0	0						
				3	4	1	0	08/09/18	21:36:12	09/05/18	12:05:10		
				4	1	0	1						
2	11	23-Aug-18	5-Sep-18	1	2	0	0						
				2	3	0	0						
				3	1	0	0						
				4	1	1	0	08/28/18	10:57:10	09/05/18	12:05:10		
	12	6-Sep-18	19-Sep-18	1	2	0	0						
				2	3	0	0						
				3	1	0	0						
				4	2	0	0						
3	13	20-Sep-18	4-Oct-18	1	1	0	0						
				2	4	0	0						
				3	2	0	0						
				4	0	0	0						
	14	5-Oct-18	18-Oct-18	1	1	0	0						
				2	0	1	0	10/11/18	1:34:11	10/19/18	1:01:11		
				3	1	1	0	10/05/18	16:47:00	10/19/18	1:01:11		
				4	4	0	0						
4	15	19-Oct-18		1	1	0	0						
				2	0	0	0						
				3	0	0	0						
				4	4	0	0						
		26-Oct-18	28-Oct-18									2	
			2-Nov-18	1	0	0	0						
				2	0	0	0						
				3	0	0	0						
				4	1	0	0						
	16	3-Nov-18	15-Nov-18	1	1	0	0						
				2	0	0	0						
				3	2	1	0	11/14/18	22:07:00	11/28/18	15:36:00		
				4	1	0	0						
5	17	16-Nov-18	27-Nov-18	1	1	0	0						
				2	0	0	0						
				3	0	0	0						
				4	0	0	0						
	18	28-Nov-18	11-Dec-18	1	6	0	0						
				2	1	1	0	12/06/18	9:19:08	12/14/18	7:49:00		
				3	1	0	0						

