



TO DO:  
 ok - New part/footprint for LTC4151 in MS package  
 ok - Welwyn resistor should be 0.02 ohms 1/2w - new part#  
 ok - U40-U43 addressing - cable select using P1 AS\_lo & AS\_hi  
 ok - add address table to drwg  
 ok - confirm I2C bus pullups - should be to uP vcc, but not available here; pullups on both boards may be too much - what about I2C bus repeat or use extra busses on BL233?  
 ok - update part refs  
 - pcb layout

LTC4151 Addressing

Cable Select: when plugged into ATIM port 1 (channels 1-4) AS\_lo is open, AS\_hi is 3.3v  
 when plugged into ATIM port 2 (channels 5-8) AS\_lo is 0v, AS\_hi is open

Device	low Addr	Hi Addr	A1	A0
1(5)	0xD0	0xD8	AS_lo	hi (3.3v)
2(6)	0xD2	0xDA	hi (3.3v)	AS_hi
3(7)	0xD4	0xDC	AS_lo	open (nc)
4(8)	0xD6	0xDE	AS_lo	low (0v)

RV/PV\_Date\_Eng Revision Details

Note: 48rtn tied to gnd at Pwr Supply ONLY

Serial No '888' on PCB

Project: Murchison Widefield Array		Australian National University Research School of Astronomy and Astrophysics Mount Stromlo Observatory Cotter Road Weston ACT 2611 www.mso.anu.edu.au	
Title: ATIM Transition and 48v monitor		Revision: 01	
Number: MSO-208-540-000-SCH	Revision: 01		Print Date: 11/01/2010
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