

ACIS Verification Summary Report

36-01510.019

Specification:	ACIS Contract End Item Specification
Requirement Number/Title:	3.1.3.2a Event Modes (VRSD 3.1.3.2a-8)
Requirement Statement:	Pixels with bias-corrected amplitudes exceeding a commandable event threshold, and which are local maxima, relative to the eight nearest neighboring pixels, shall be deemed centers of candidate events.
Verification Method:	Test and Demonstration
Procedure Number:	36-57301.32

Configuration:

Cycle Time:

Verification Discussion/Results

See software verification report 3657303.05.

Date Run: 19 Mar 1997

Verification Report: 36-57303.05
 Test Procedure: 36-57301.05 ACIS SW Thresholding Verification Procedure
 Test Script: ?
 Script Files: teft.cmd teft.fep
 Working Directory: /nfs/acis/h5/fkb/tools/swtests/threshold/test4.2
 Last Modified: ?
 Procedure Paragraphs Verified: 4.2
 Software Version: Flight 1.?
 IP&CL Version: ipcl_struct.xs3,v ?
 Hardware Configuration: Engineering Unit
 Host: cypress/sun4/sparc/?
 Test Conducted by: Frederick K. Baganoff

Pass/Fail Criteria: The number of ^{events} detected and their ~~corrected~~ pulse heights must match the results expected for the specified values of the test parameters.

Test Parameter Values:

Event Pulse Heights	=	1	2	3	4	2048	4091	4092	4093	4094	4095
Threshold Set Point	=	0									
Pixel Bias Map Value	=	0									
Initial Overclock	=	0									
Computed Pixel Bias	=	0									
Bias Level	=	4092									
Overclock Level	=	4092									
Delta Overclock	=	4092									
Threshold Register Value	=	4092									
Event Threshold	=	4092									

Expected Event Pulse Heights = 4093 4094 4095 4095 4095 4095 4095 4095 4095 4095 4095
 Detected Event Pulse Heights = 4093 4094 4095 4095 4095 4095 4095 4095 4095 4095 4095

Test Result: Passed