NOTES:
1. SOLDERING TO BE IN VICINITY OF NASA SPECIFICATION NAS-STD-8739.2 AND NAS-STD-8739.3.
2. KITTED HEATERS ARE NOT INSTALLED.
3. VACUUM BAKE AT 41 (F/N. 3) AT 80C FOR 72 HOURS PRIOR TO KITTING.
4. REMOVE EXISTING HARDWARE AFTER CONFORMAL COAT. INSTALL F/N. 6, 7, 8, 9 AND 2 AS SHOWN.
5. TORQUE 10-32 UNC SCREW TO 32 IN-CH. SPOT WELD F/N. 17. MIX RATIO: 4/3.0/1.2/5.2/0.4/2.4.
6. PLACE CHECKSUM LABEL ON U1 USING F/N. 22 AND COAT WITH F/N. 22.
7. SOLDER TOP ENDS OF U1 AND U2, 11/17/14/17, 0/22/35/06, OR, 11, 17 USING F/N. 16 PRIOR TO CONFORMAL COAT.
8. PARTS TO RECEIVE CIRCUIT TRACES VISUAL INSPECTION PRIOR TO KITTING AND INSTALLATION.
9. VERIFY D3 IS NOT SHORTED TO VIA UNDERNEATH CAPACITOR.
10. EPOXY F/N. 5 AND 3.5 TO F/N. 1 USING F/N. 13. AFTER CONFORMAL COAT.
11. MOUNTING DIMENSIONS SPECIFIED FOR THE EXISTING PORTION OF WEISELOK ASSY.
12. ITEM IS STATIC SENSITIVE. HANDLE W/ INSTRUMENTS, OPERATE W/ PROCEDURE 99-01003 OR EQUIVALENT.
13. MEASURE UNMARKED CAPACITORS PRIOR TO KITTING. BAG AND TAG.
14. MASK FOR CONFORMAL COAT AS NEEDED.
15. CONFORMAL COAT ASSEMBLY WITH F/N. 4.
16. VACUUM BAKE AT 80C. VACUUM BAKE AT 40C. 10H. THERMISTOR PRIOR TO VACUUM BAKE.
17. TORQUE F/N. 19 TO 50 IN-CH AND SPOTWELD WITH F/N. 17.