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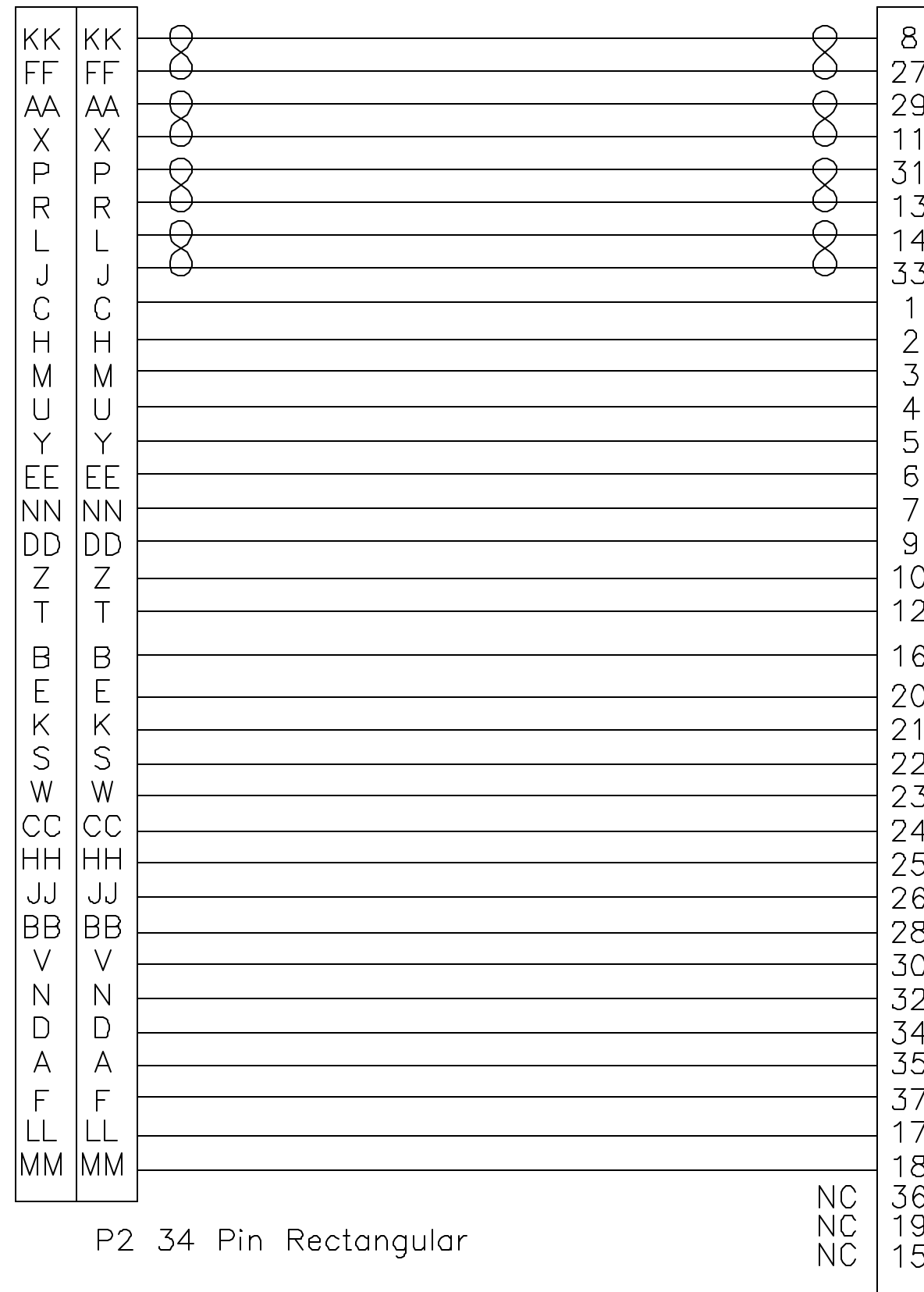
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REVISIONS					
ECO	REV	DESCRIPTION	CHECKED	APPROVED	DATE
30-006	A	INITIAL RELEASE			

D

D

- OUT-A Video Output-A
- RET-A Video Return-A
- OUT-B Video Output-B
- RET-B Video Return-B
- OUT-C Video Output-C
- RET-C Video Return-C
- OUT-D Video Output-D
- RET-D Video Return-D
- P1/IA Phase-1 Imaging Array
- DR-D Drain-D
- RD-ABCD Reset Diodes A-D
- P3-FS Phase-3 Frame Store
- DR-B Drain-B
- P1/OR-AC Phase-1 Output Registers A & C
- BJ Back Junction
- P2/OR-AC Phase-2 Output Registers A & C
- P1/OR-BD Phase-1 Output Registers B & D
- P1/FS Phase-1 Frame Store
- P3/IA Phase-3 Imaging Array
- OG-ABCD Output Gates A-D
- RG-ABCD Reset Gates A-D
- P2/FS Phase-2 Frame Store
- SCP Scupper
- P2/OR-BD Phase-2 Output Registers B & D
- DR-A Drain A
- SUB2 Chip Substrate 2
- P3-OR-ABCD Phase-3 Output Registers A-D
- SUB3 Chip Substrate 3
- DR-C Drain-C
- P2/IA Phase-2 Imaging Array
- RTD1
- RTD2
- ID Input Diode
- IG Input Gate



P2 34 Pin Rectangular

J1, 37 PIN D-SUB

C

C

B

B

A

A

		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCE: ANGLES +/- 1' 3 PLACE DECIMALS +/- .005 2 PLACE DECIMALS +/- .01	NAME	DATE	Massachusetts Institute of Technology Center for Space Research ASTRO-E2			
			DRAWN	M. SMITH	SCHEMATIC CCD TEST FIXTURE CABLE, ASTRO-E2			
			CHECKED					
			MATERIAL	APPROVED				
30-50114.06	ASTRO-E2	FINISH	RELEASED		SIZE	FSCM NO.	DWG NO.	REV
NEXT ASSEMBLY	USED ON		CAD FILE	50114_06_01_rA	C	80230	30-50114.0601	A
APPLICATION			SCALE	1:1	SHEET		1 OF 1	

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