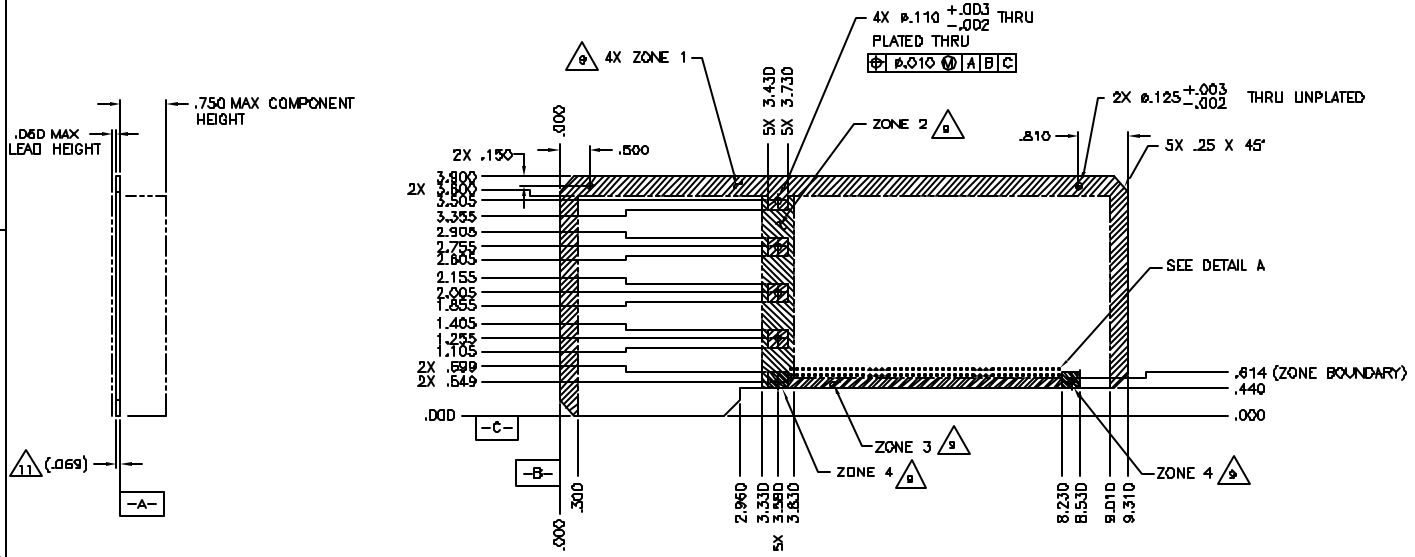
REVISIONS				
ECO	REV	DESCRIPTION	DATE	APPROVED
30-PCB	A	INITIAL RELEASE (FROM 29-20201.01 _rA)		



NOTES:

1. BOARD SHOWN FROM CONNECTOR SIDE.
2. UNTOLERANCED DIMENSIONS LOCATING TRUE POSITION ARE BASIC.
4. HOLE SIZES ARE AFTER PLATING.
5. PRINTED WIRING BOARD PART NUMBER IS 30-20201 OR 29-20201.
6. DESIGN AND FABRICATION SHALL BE IAW 30-20201.03 OR 29-20201.03.
7. SOLDER MASK MUST BE APPLIED OVER BARE COPPER, EXCEPT AS NOTED.
8. MARKING SHALL INCLUDE AS A MINIMUM:

MARKING	OBTAINED FROM
MANUFACTURER'S NAME	PWB MANUFACTURER
MANUFACTURER'S PART NUMBER	PWB MANUFACTURER
MANUFACTURER'S LOT CODE	PWB MANUFACTURER
PWB PART NUMBER	30-20201
PWB REVISION	PWB PURCHASE ORDER
PWB NAME	THERMAL CONTROL
ASSEMBLY PART NUMBER	30-20200 REV
CONNECTOR PIN DESIGNATION	THIS DRAWING

MARKING LABLED 29-2020x.xx IS ACCEPTABLE.

9. ZONE RESTRICTIONS:

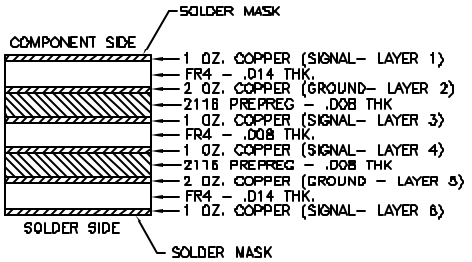
ZONE 1: NO COMPONENTS OR EXTERNAL TRACES (EXCEPT GROUND PLANE) WITHIN THIS AREA. GROUND PLANE TO TERMINATE WITH .050 FROM BOARD EDGE. AREA TO BE FREE OF SOLDER MASK BOTH SIDES. AREA TO BE BARE COPPER, FAR SIDE ONLY, EXCEPT FOR .010 NO COPPER AT BOARD EDGE AND .020 AT INTERNAL BOUNDRIES.

ZONE 2: .125 MAXIMUM COMPONENT HEIGHT IN THIS AREA, EXCEPT CONNECTOR.

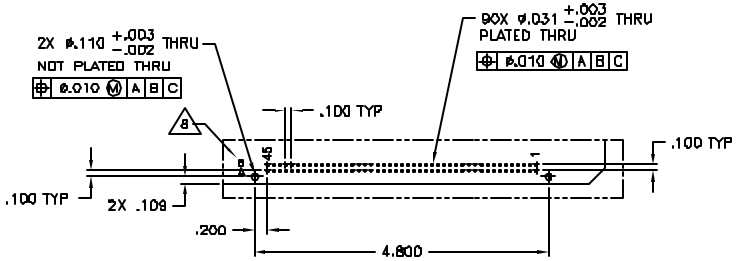
ZONE 3: NO TRACES ON COMPONENT SIDE THIS AREA.

ZONE 4: NO EXTERNAL OR INTERNAL TRACES (EXCEPT GROUND PLANE) WITHIN THESE AREAS.

10. ALL PLATED THRU HOLES TO BE FREE OF SOLDER MASK.
11. OVERALL THICKNESS INCLUDES PLATING.



LAYER ASSIGNMENTS



DETAIL A

30-20201	MARCH-02	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ANGLES +/- 1° 3 PLACE DECIMALS +/- .005 2 PLACE DECIMALS +/- .01	DRAWN N. SMITH 1/29/02	Massachusetts Institute of Technology Center for Space Research ASTRO-02
NEXT ASSEMBLY	USED ON	MATERIAL N/A	DESIGNED	OUTLINE, PCB, THERMAL CONTROL ELECTRONICS
APPLICATION		FINISH N/A	APPROVED	SIZE D FSCM NO. 80230 DWG NO. 30-20201.01 REV A
			RELEASED	SCALE 1:1 SHEET 1 OF 1
			DWG FILE 30-20201_01_rA	