

4

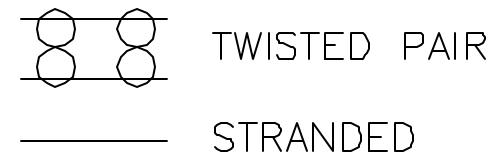
3

2

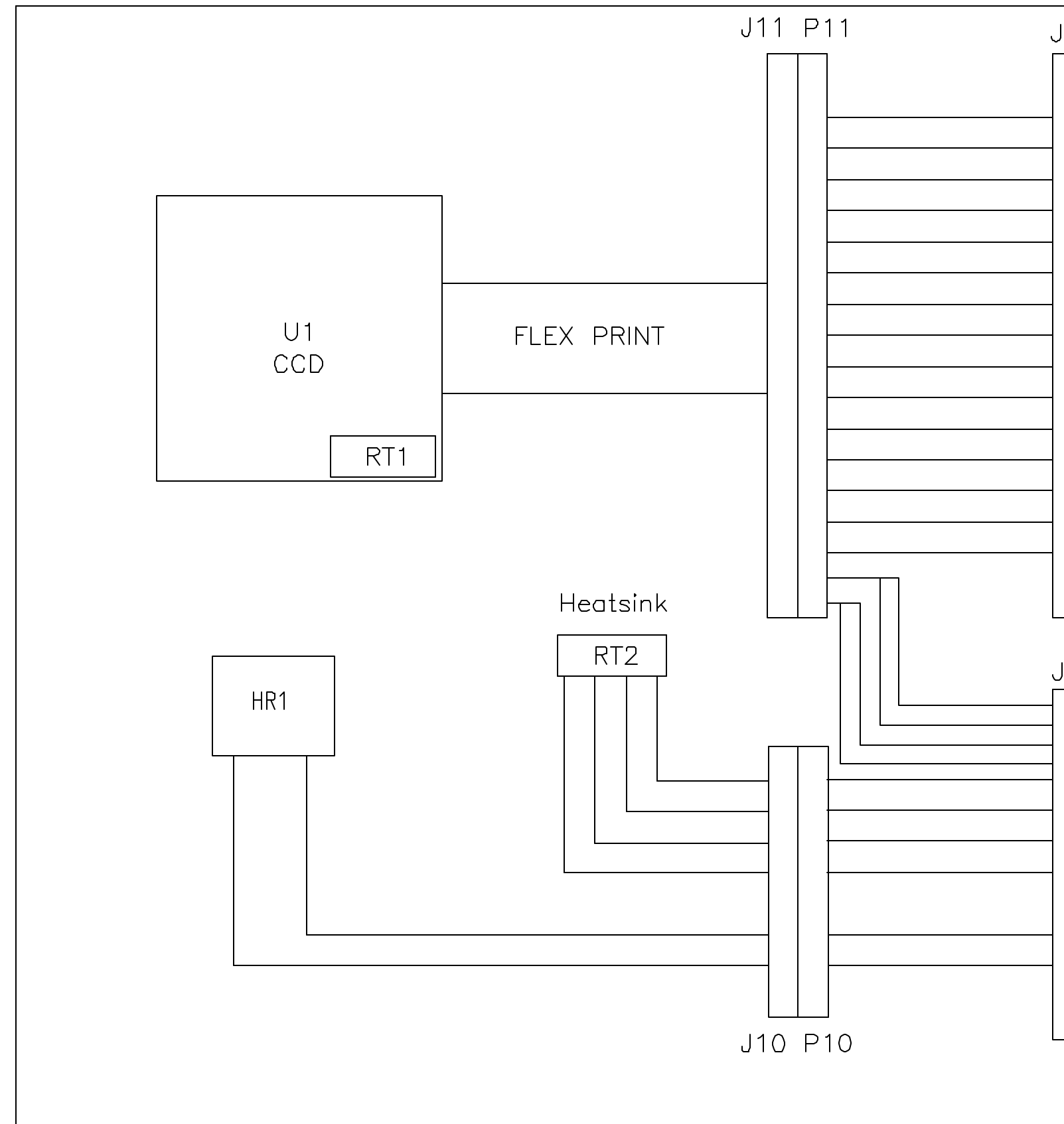
1

NOTES:

- 1. UNLESS OTHERWISE NOTED, ALL WIRE TO BE 26 AWG.
- 2. LEGEND:



REVISIONS					
REV.	ECO NO.	DESCRIPTION	CHECKED	APPROVED	DATE
A	30-005	INITIAL RELEASE	EAB	RF	7-19-02
B	30-019	CORRECT MISWIRES TO P11-A AND F			



XIS-BDY-S0  
XIS-BDY-S1  
XIS-BDY-S2  
XIS-BDY-S3

B	B																		
1	2																		

REVISION STATUS OF SHEETS

	NAME	DATE
DRAWN	M SMITH	7-15-02
CHECKED	EAB	7-19-02
APPROVED	RICK F	7-19-02
RELEASED	JEAN P	8-01-02
WEIGHT		

UNLESS OTHERWISE SPECIFIED:  
DIMENSIONS SHALL BE INTERPRETED IAW ANSI Y14.5 - 1982  
DIMENSIONS ARE IN INCHES  
TOLERANCE: ANGLES ±1°  
3 PLACE DECIMALS ±.005  
2 PLACE DECIMALS ±.01  
THIRD ANGLE PROJECTION

MASSACHUSETTS INSTITUTE OF TECHNOLOGY  
CENTER FOR SPACE RESEARCH  
CAMBRIDGE, MA 02139

INTERCONNECTION DIAGRAM-  
SENSOR BASE

SIZE	CAGE CODE	DWG. No.	REV.
C	80230	30-05021.03	B

SCALE NONE SHEET 1 OF 2

4

3

2

1

D

D

C

C

B

B

A

A

4

3

2

1

REVISIONS					
REV.	ECO NO.	DESCRIPTION	CHECKED	APPROVED	DATE
		SEE SHEET 1			

D

D

C

C

B

B

A

A

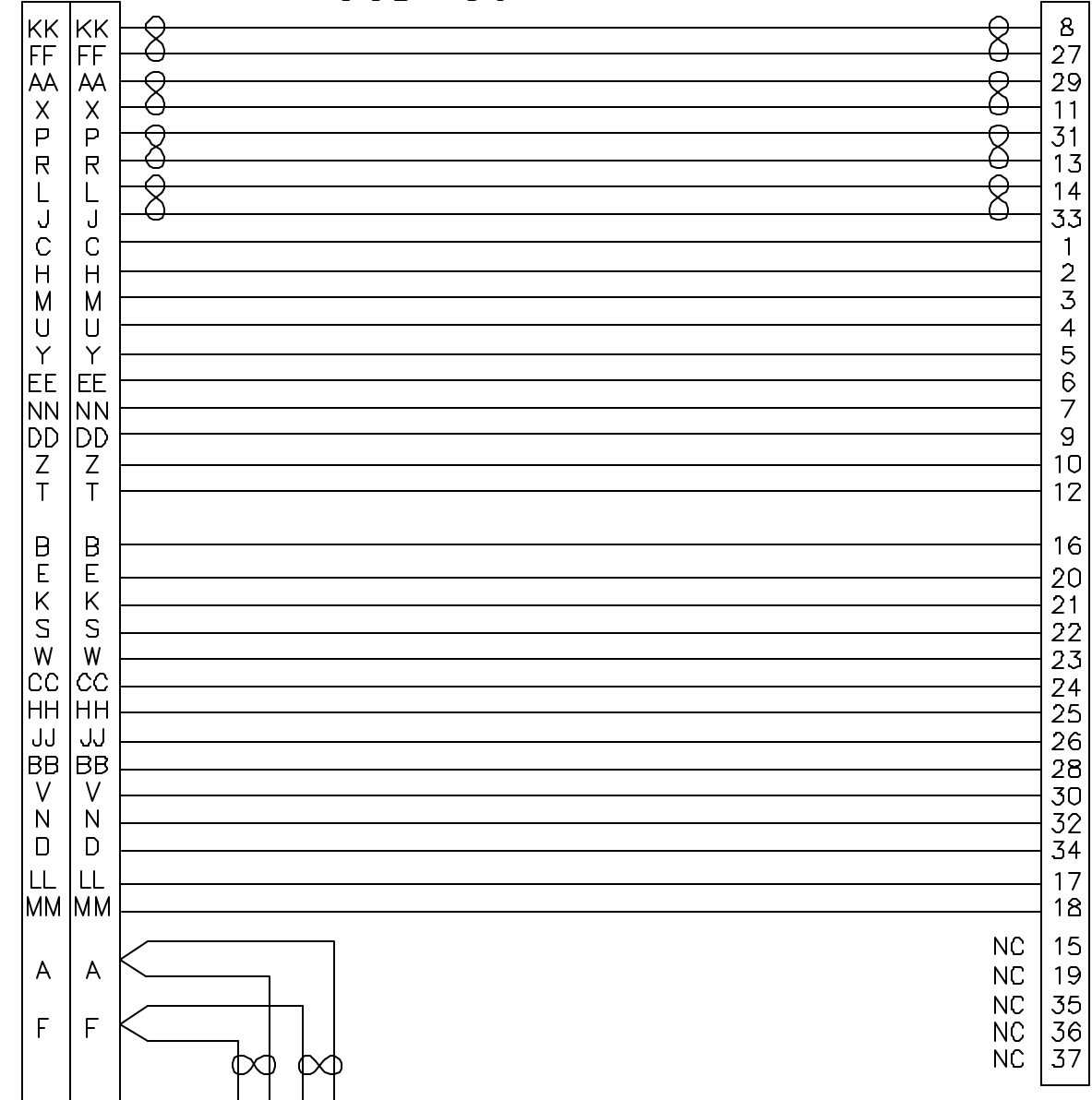
- (P/O CCD Flex Print)
- OUT-A Video Output-A
- RET-A Video Return-A
- OUT-B Video Output-B
- RET-B Video Return-B
- OUT-C Video Output-C
- RET-C Video Return-C
- OUT-D Video Output-D
- RET-D Video Return-D
- P1/IA Phase-1 Imaging Array
- DR-D Drain-D
- RD-ABCD Reset Diodes A-D
- P3-FS Phase-3 Frame Store
- DR-B Drain-B
- P1/OR-AC Phase-1 Output Registers A & C
- BJ Back Junction
- P2/OR-AC Phase-2 Output Registers A & C
- P1/OR-BD Phase-1 Output Registers B & D
- P1/FS Phase-1 Frame Store
- P3/IA Phase-3 Imaging Array
- OG-ABCD Output Gates A-D
- RC-ABCD Reset Gates A-D
- P2/FS Phase-2 Frame Store
- SCP Scupper
- P2/OR-BD Phase-2 Output Registers B & D
- DR-A Drain A
- SUB2 Chip Substrate 2
- P3-OR-ABCD Phase-3 Output Registers A-D
- SUB3 Chip Substrate 3
- DR-C Drain-C
- P2/IA Phase-2 Imaging Array
- ID Input Diode
- IG Input Gate

J11 34 Socket Rectangular

P11 34 Pin Rectangular

W15  
CCD-S0

J2 (P/O 30-10201)



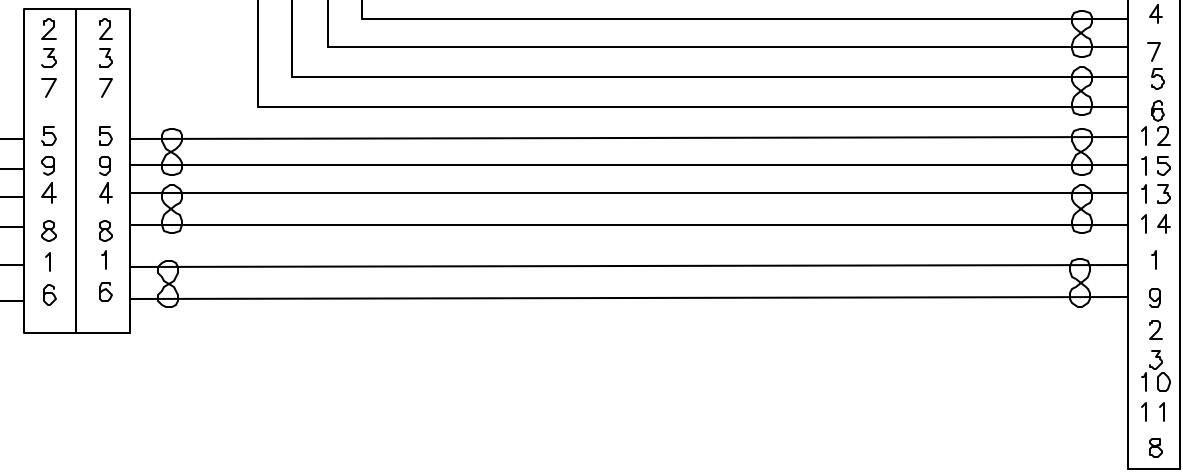
RTD1  
RTD2

J10 9 Pin D-Sub  
P10 9 Socket D-Sub

- RT2 (yellow) (+ Current) BASEI+
- RT2 (blue) (- Current) BASEI-
- RT2 (yellow) (+ Voltage Sense) BASEV+
- RT2 (blue) (- Voltage Sense) BASEV-
- (TEC) CURRENT(+) TEC+(1)
- (TEC) CURRENT(-) TEC-(1)

RT2  
HR1

- YELLOW
- BLUE
- YELLOW
- BLUE
- RED (24 AWG)
- BLACK (24 AWG)



J1 (P/O 30-10201)

- 4 RT1 (+ CURRENT)
- 7 RT1 (- CURRENT)
- 5 RT1 (+ VOLTAGE SENSE)
- 6 RT1 (- VOLTAGE SENSE)
- 12
- 15
- 13
- 14
- 1 (TEC Current+) TEC+(1)
- 9 (TEC Current-) TEC-(1)
- 2
- 3
- 10
- 11
- 8

SIZE C	CAGE CODE 80230	DWG. No. 30-05021.03	REV. B
SCALE NONE		SHEET 2	

4

3

2

1